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## austriamicrosystems CMOS and BiCMOS technologies

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- Cadence and/or Mentor design kit Front-end (schematics, symbols, simulation parameters, placement outlines) and/or synopsys design kit

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**For EUROPRACTICE**

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Date :  
Signature :

**For the company/institute**

Name : .....  
Title : .....  
Institution : .....  
Address : .....  
.....  
EUROPRACTICE ref. number :.....

Date :  
Signature :

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**Details of technical contact person to whom the design kits and updates should be sent**

**Name :** \_\_\_\_\_ **E-mail :** \_\_\_\_\_  
**Title :** \_\_\_\_\_ **Tel :** \_\_\_\_\_