



## austriamicrosystems 0.35 $\mu\text{m}$ HV CMOS (H35)

### Description

The 0.35 $\mu\text{m}$  high voltage process platform is optimized for complex mixed signal circuits up to 50V operating conditions. In addition to the standard CMOS transistors, a variety of high voltage transistors are available: HV-NMOS, -PMOS, -DMOS transistors, N-junction FETS, isolated NPN bipolar transistors, and isolated LV-NMOS transistors.

High voltage and standard devices can be easily combined into the same chip. Low power consumption and fast switching speed provide a wide range of applications in the automotive and industrial segments. Further applications are targeted towards high precision analog Frontends for sensors and transducers.

### Applications

Applications for the 12V Automotive Standard Environment with High Voltage Spikes  
Analog Frontends for Sensors & Transducers  
Display Drivers & Industrial Markets

### Key Features

H35B3D2 2P/3M HV CMOS Mixed Signal, RF, PIP+MIM, HR poly, 20V gate module  
H35B4D3 2P/4M HV CMOS Mixed Signal, RF, PIP+MIM, thick metal, HR poly, 20V gate module

### Design Kit

Synopsys:  
Digital standard cell libraries  
CADENCE:  
Digital and mixed-signal HIT-Kit for Analog Artist  
Build Gates synthesis libraries  
VERILOG Simulation Models  
Digital standard cell libraries for Silicon Ensemble  
Spectre simulation parameters  
Device layout generators (pcells)  
RAM, ROM, DPRAM compilers in qualification  
Analog macros

Simulation Parameters for: ELDO, HSPICE, PSPICE, SABER, SMASH, SPECTRE, ADS